REPLACMENT SHEET 9 / 27



FIG. 9

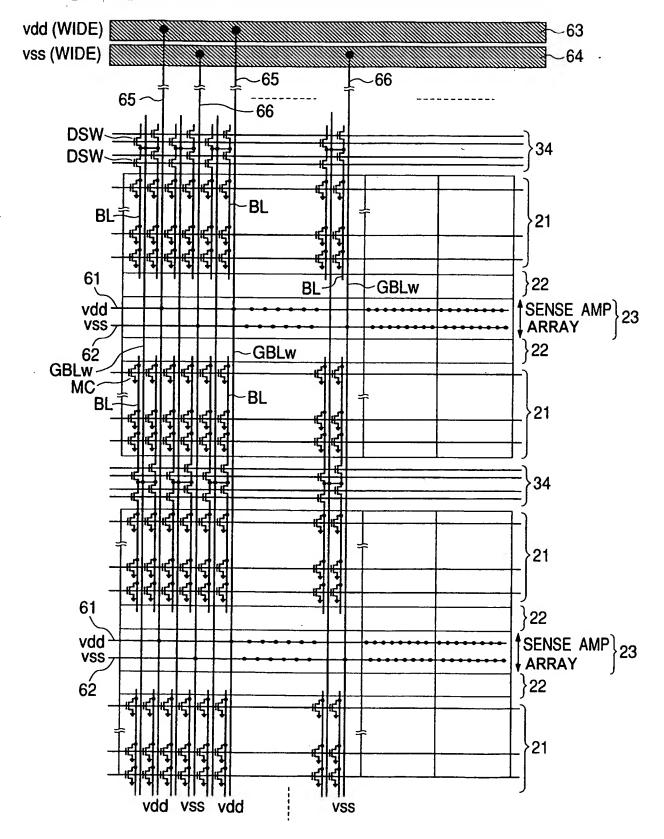
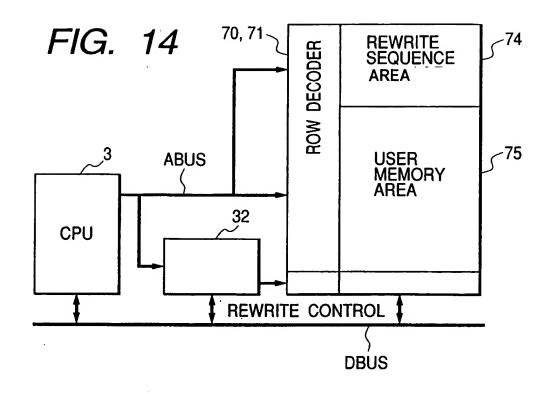
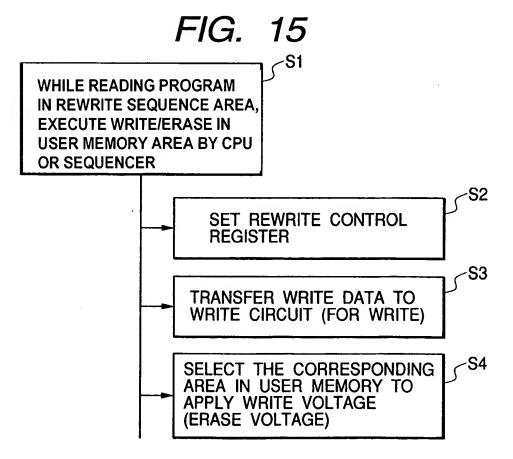


FIG. 10 61 vdd (WIDE) SENSE AMP CIRCUIT 62 23 vss (WIDE) vdd (WIDE) SENSE AMP 62 23 vss (WIDE) MC -BL BL WC GBLw

-GBLw

REPLACMENT SHEET 14 / 27





REPLACMENT SHEET 16 / 27

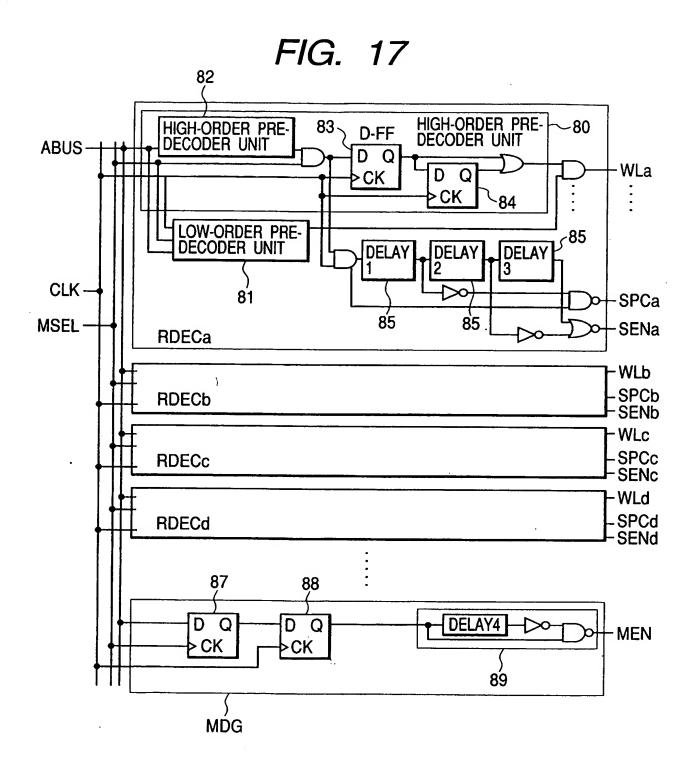
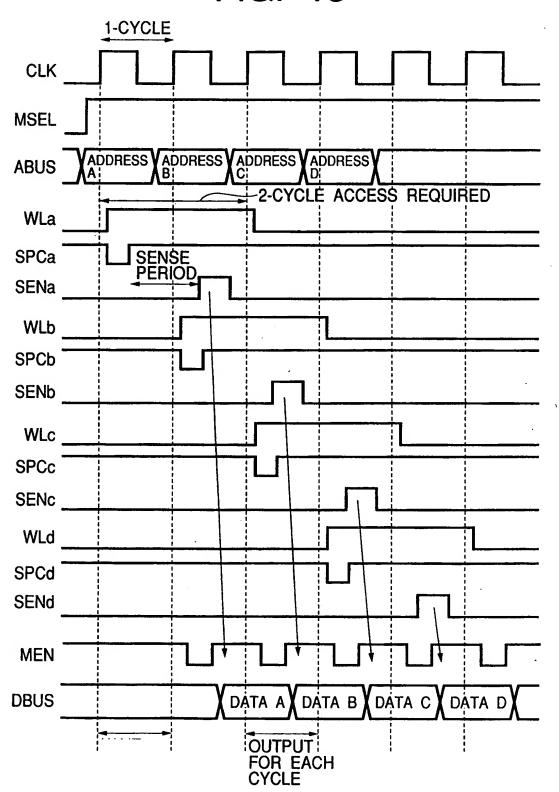
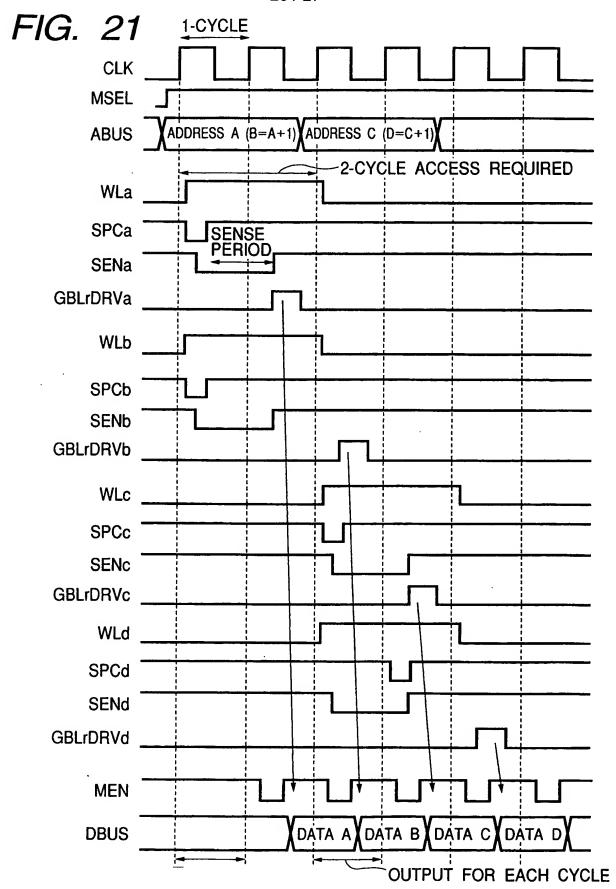


FIG. 18





REPLACMENT SHEET 23 / 27

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